

\* If the form is filed by more than one reporting person, see Instruction 4(b)(v).

**\*\*** Intentional misstatements or omissions of facts constitute Federal Criminal Violations. *See* 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).

- (1)** Each restricted stock unit represents the right to receive, following vesting, one share of Intel Corporation common stock.
- (2)** Unless earlier forfeited under the terms of the RSU, 33 1/3% of the award vests and converts into common stock on each anniversary of the grant date, unless that date falls on a non-business date, in which case the next business date shall apply.

Each Performance-based Restricted Stock Unit (RSU) represents the right to receive, following vesting, no less than 50% and no more than 200% of one share of Intel common stock, together
- (3)** with dividend equivalent shares on the vested number of shares. The resulting number of shares of Intel common stock acquired upon vesting of the Performance-based RSUs is contingent upon the achievement of pre-established performance metrics, as approved by the Company's Compensation Committee, over a three-year period beginning January 24, 2012 and ending on January 24, 2015, unless that date falls on a date that the NASDAQ Stock Market is closed, in which case the next business date that the NASDAQ Stock Market is open shall apply.
- (4)** Unless earlier forfeited under the terms of the Performance-based RSU, each Performance-based RSU vests and converts into no less than 50% and no more than 200% of one share of Intel common stock three years after the grant date (together with dividend equivalent shares thereon), unless that date falls on a non-business date, in which case the next business date shall apply.

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, *see* Instruction 6 for procedure.

Potential persons who are to respond to the collection of information contained in this form are not required to respond unless the form displays a currently valid OMB number.