

# FORM 4

**UNITED STATES SECURITIES AND EXCHANGE COMMISSION  
Washington, D.C. 20549**

OMB APPROVAL	
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Check this box if no longer subject to Section 16. Form 4 or Form 5 obligations may continue. See Instruction 1(b).

## STATEMENT OF CHANGES IN BENEFICIAL OWNERSHIP OF SECURITIES

Filed pursuant to Section 16(a) of the Securities Exchange Act of 1934 or Section 30(h) of the Investment Company Act of 1940

(Print or Type Responses)

1. Name and Address of Reporting Person * BRYANT ANDY D		2. Issuer Name and Ticker or Trading Symbol INTEL CORP [INTC]		5. Relationship of Reporting Person(s) to Issuer (Check all applicable) <input checked="" type="checkbox"/> Director <input type="checkbox"/> Officer (give title below) <input type="checkbox"/> 10% Owner <input type="checkbox"/> Other (specify below)	
(Last) (First) (Middle) C/O INTEL CORPORATION, 2200 MISSION COLLEGE BLVD		3. Date of Earliest Transaction (Month/Day/Year) 03/02/2020			
(Street) SANTA CLARA, CA 95054		4. If Amendment, Date Original Filed(Month/Day/Year)		6. Individual or Joint/Group Filing(Check Applicable Line) <input checked="" type="checkbox"/> Form filed by One Reporting Person <input type="checkbox"/> Form filed by More than One Reporting Person	
(City) (State) (Zip)		<b>Table I - Non-Derivative Securities Acquired, Disposed of, or Beneficially Owned</b>			

1. Title of Security (Instr. 3)	2. Transaction Date (Month/Day/Year)	2A. Deemed Execution Date, if any (Month/Day/Year)	3. Transaction Code (Instr. 8)		4. Securities Acquired (A) or Disposed of (D) (Instr. 3, 4 and 5)			5. Amount of Securities Beneficially Owned Following Reported Transaction(s) (Instr. 3 and 4)	6. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	7. Nature of Indirect Beneficial Ownership (Instr. 4)
			Code	V	Amount	(A) or (D)	Price			
Common Stock	03/02/2020		M		19,014	A	(1)	425,595	D	
Common Stock	03/02/2020		F		9,023	D	\$ 56.37	416,572	D	
Common Stock	03/03/2020		S		9,481 (2)	D	\$ 57.347 (3)	407,091	D	
Common Stock	03/03/2020		S		510 (2)	D	\$ 57.9741 (4)	406,581	D	
Common Stock								1,148	I	Joint Account with Spouse
Common Stock								3,671.006 (5)	I	By Employee Benefit Plan Trust

Reminder: Report on a separate line for each class of securities beneficially owned directly or indirectly.

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**Table II - Derivative Securities Acquired, Disposed of, or Beneficially Owned (e.g., puts, calls, warrants, options, convertible securities)**

1. Title of Derivative Security (Instr. 3)	2. Conversion or Exercise Price of Derivative Security	3. Transaction Date (Month/Day/Year)	3A. Deemed Execution Date, if any (Month/Day/Year)	4. Transaction Code (Instr. 8)		5. Number of Derivative Securities Acquired (A) or Disposed of (D) (Instr. 3, 4, and 5)	6. Date Exercisable and Expiration Date (Month/Day/Year)		7. Title and Amount of Underlying Securities (Instr. 3 and 4)		8. Price of Derivative Security (Instr. 5)	9. Number of Derivative Securities Beneficially Owned Following Reported Transaction(s) (Instr. 4)	10. Ownership Form of Derivative Security: Direct (D) or Indirect (I) (Instr. 4)	11. Nature of Indirect Beneficial Ownership (Instr. 4)
				Code	V		Date Exercisable	Expiration Date	Title	Amount or Number of Shares				
Performance-based Restricted Stock Units	(1)	03/02/2020		M		28,189	03/02/2020(6)	(6)	Common Stock	28,189 (7)	(1)	0	D	

## Reporting Owners

Reporting Owner Name / Address	Relationships			
	Director	10% Owner	Officer	Other
BRYANT ANDY D C/O INTEL CORPORATION	X			

2200 MISSION COLLEGE BLVD SANTA CLARA, CA 95054				
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## Signatures

/s/ Alex Shukhman, attorney-in-fact

\*\*Signature of Reporting Person

03/04/2020

Date

## Explanation of Responses:

\* If the form is filed by more than one reporting person, *see* Instruction 4(b)(v).

\*\* Intentional misstatements or omissions of facts constitute Federal Criminal Violations. *See* 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).

Each performance-based Restricted Stock Unit (RSU) represents the right to receive, following vesting, up to 200% of one share of Intel common stock. The number of shares of Intel common stock acquired upon vesting of the performance-based RSUs is contingent upon the achievement of pre-established performance metrics, as approved by the Company's Compensation Committee, over a three-year performance period beginning on February 1, 2017 and ending on the third anniversary of February 1, 2017, unless that date falls on a date that the NASDAQ Stock Market is closed, in which case the next business date that the NASDAQ Stock Market is open shall apply.

(2) This transaction was made pursuant to trading instructions adopted by the undersigned on November 20, 2019 that are intended to comply with Rule 10b5-1(c).

(3) This transaction was executed in multiple trades at prices ranging from \$56.99 to \$57.827. The price reported above reflects the weighted average sale price. The reporting person hereby undertakes to provide upon request to the SEC staff, the issuer or a security holder of the issuer full information regarding the number of shares and prices at which the transaction was effected.

(4) This transaction was executed in multiple trades at prices ranging from \$57.90 to \$58.08. The price reported above reflects the weighted average sale price. The reporting person hereby undertakes to provide upon request to the SEC staff, the issuer or a security holder of the issuer full information regarding the number of shares and prices at which the transaction was effected.

(5) Includes 21,547 shares acquired via dividend reinvestment in March 2020.

(6) Unless earlier forfeited under the terms of the performance-based RSU, each performance-based RSU vests and converts into no more than 200% of one share of Intel common stock three years and one month after the grant date, unless that date falls on a non-business date, in which case the next business date shall apply.

(7) The total number of shares of Common Stock received upon achievement of the pre-established performance metrics was 19,014 (or 67.452% of the target number of performance-based RSUs granted), as reported in Table I.

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, *see* Instruction 6 for procedure.

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