FORM 4

Check this box if no
longer subject to Section
16. Form 4 or Form 5
obligations may
continue. See Instruction
1(b).

UNITED STATES SECURITIES AND EXCHANGE COMMISSION Washington, D.C. 20549

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SEC 1474 (9-02)

STATEMENT OF CHANGES IN BENEFICIAL OWNERSHIP OF SECURITIES

Filed pursuant to Section 16(a) of the Securities Exchange Act of 1934 or Section 30(h) of the Investment Company Act of 1940

(Print or Type Responses)										
1. Name and Address of Reporting Rodgers Steven Ralph	2. Issuer Name and INTEL CORP [II		rading	Symbol		5. Relationship of Reporting Person(s) to Issuer (Check all applicable) Director 10% Owner				
(Last) (Firs C/O INTEL CORPORATIO COLLEGE BLVD	3. Date of Earliest Tr 01/30/2021	ansaction (N	fonth/	Day/Year)	I	_X_Officer (give title below)Other (specify below) EVP General Counsel				
(Stre SANTA CLARA, CA 95054	4. If Amendment, Da	te Original F	Filed(N	1onth/Day/Ye	ear)	6. Individual or Joint/Group Filing(Check Applicable Line) _X_Form filed by One Reporting Person Form filed by More than One Reporting Person				
(City) (Stat	Table I - Non-Derivative Securities Acquired, Disposed of, or Beneficially Owned									
1. Title of Security (Instr. 3)	2. Transaction Date (Month/Day/Ye	2A. Deemed Execution Date, if any (Month/Day/Year)	(Instr. 8)	ion	4. Securities Acquired (A) or Disposed of (D) (Instr. 3, 4 and 5)			5. Amount of Securities Beneficially Owned Following Reported Transaction(s) (Instr. 3 and 4)		Beneficial Ownership
			Code	v	Amount	(A) or (D)	Price		or Indirect (I) (Instr. 4)	(Instr. 4)
Common Stock	02/01/2021		М		2,947	А	<u>(1)</u>	101,842	D	
Common Stock	02/01/2021		F		1,020	D	\$ 56.55	100,822	D	
Common Stock	02/01/2021		М		2,236	А	(1)	103,058	D	
Common Stock	02/01/2021		F		1,066	D	\$ 56.55	101,992	D	
Common Stock	02/01/2021		М		1,728	А	<u>(1)</u>	103,720	D	
Common Stock	02/01/2021		F		857	D	\$ 56.55	102,863	D	

Reminder: Report on a separate line for each class of securities beneficially owned directly or indirectly.

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 Table II - Derivative Securities Acquired, Disposed of, or Beneficially Owned

(e.g., puts, calls, warrants, options, convertible securities)															
1. Title of Derivative Security (Instr. 3)	Conversion	(Month/Day/Year)	Execution Date, if	Code		5. Number of Derivative		6. Date Exercisable and Expiration Date (Month/Day/Year)		7. Title and Amount		Derivative Security (Instr. 5)	Securities Beneficially Owned Following Reported	Ownership Form of Derivative Security: Direct (D) or Indirect	11. Nature of Indirect Beneficial Ownership (Instr. 4)
				Code	v	(A)	(D)	Date Exercisable	Expiration Date	Title	Amount or Number of Shares		Transaction(s) (Instr. 4)	(1) (Instr. 4)	
Restricted Stock Units	(1)	01/30/2021		А		23,186		04/30/2021(2)	<u>(2)</u>	Common Stock	23,186	(1).	23,186	D	
Performance- Based Stock Units (PSUs)	<u>(3)</u>	01/30/2021		А		92,744		(<u>4</u>).	<u>(4)</u>	Common Stock	92,744	(<u>3)</u> .	92,744	D	
Restricted Stock Units	(1)	02/01/2021		М			2,947	(<u>5)</u>	<u>(5)</u>	Common Stock	2,947	(1).	0	D	
Restricted Stock Units	(1)	02/01/2021		М			2,236	(<u>6)</u>	<u>(6)</u>	Common Stock	2,236	(1).	8,945	D	
Restricted Stock Units	(1).	02/01/2021		М			1,728	(Z).	<u>(</u> 2)	Common Stock	1,728	(1).	13,827	D	

Reporting Owners

Reporting Owner Name / Address	Relationships							
Reporting Owner Wante / Address	Director	10% Owner	Officer	Other				
Rodgers Steven Ralph C/O INTEL CORPORATION 2200 MISSION COLLEGE BLVD SANTA CLARA, CA 95054			EVP General Counsel					

Signatures

/s/ Alex Shukhman, attorney-in-fact

Signature of Reporting Person

02/02/2021 Date

Explanation of Responses:

- * If the form is filed by more than one reporting person, see Instruction 4(b)(v).
- ** Intentional misstatements or omissions of facts constitute Federal Criminal Violations. See 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).
- (1) Each restricted stock unit (RSU) represents the right to receive, following vesting, one share of Intel Corporation common stock.
- Each restricted stock unit (RSU) represents the right to receive, following vesting, one share of Intel Corporation common stock. Unless earlier forfeited under the terms of the RSU, 1/12th of the (2) awards vest and convert into common stock in twelve substantially equal quarterly tranches, beginning on April 30, 2021. If the quarterly vesting date falls on a non-business date, the next business date shall apply.
- Each performance-based stock unit (PSU) represents the right to receive, following vesting, up to 200% of one share of Intel common stock. The number of shares of Intel common stock acquired upon (3) vesting of the PSUs is contingent upon the achievement of pre-established performance metrics, as approved by the Company's Compensation Committee, over a three-year performance period beginning with the first day of the fiscal year of the grant date and ending on the last day of the fiscal year of the second anniversary of the grant date.
- (4) Unless earlier forfeited under the terms of the PSU, each PSU vests and converts into no more than 200% of one share of Intel common stock on January 31, 2024, unless that date falls on a non-business date, in which case the next business date shall apply.
- (5) Unless earlier forfeited under the terms of the RSU, 1/12th of the awards vest and convert into common stock in twelve substantially equal quarterly tranches, beginning on April 30, 2018. If the quarterly vesting date falls on a non-business date, the next business date shall apply.
- (6) Unless earlier forfeited under the terms of the RSU, 1/12th of the awards vest and convert into common stock in twelve substantially equal quarterly tranches, beginning on April 30, 2019. If the quarterly vesting date falls on a non-business date, the next business date shall apply.
- (7) Unless earlier forfeited under the terms of the RSU, 1/12th of the awards vest and convert into common stock in twelve substantially equal quarterly tranches, beginning on April 30, 2020. If the quarterly vesting date falls on a non-business date, the next business date shall apply.

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, see Instruction 6 for procedure.

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